

	Type	L #	Hits	S arch Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	27523	(pla pld fpga (programmable adj logic adj (device array)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/21 16:00			0
2	BRS	L2	49	1 and ((pass adj2 gate adj logic) (transmission adj gate adj logic) (pass adj transistor adj logic))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/01/21 16:03			0